

FRAM71 Configuration Sheet

Title :									
Date :									
POKE "2C000", "				Comment	(LCIM)	De-vice Type	Size [KB]	Module PORT 5.xx	
Chip_0*)	2C000	CONF							
	2C001	F-BLOCK							
Chip_1	2C002	CONF							
	2C003	F-BLOCK							
Chip_2	2C004	CONF							
	2C005	F-BLOCK							
Chip_3	2C006	CONF							
	2C007	F-BLOCK							
Chip_4	2C008	CONF							
	2C009	F-BLOCK							
Chip_5	2C00A	CONF							
	2C00B	F-BLOCK							
Chip_6	2C00C	CONF							
	2C00D	F-BLOCK							
Chip_7	2C00E	CONF							
	2C00F	F-BLOCK							
Chip_8	2C010	CONF							
	2C011	F-BLOCK							
Chip_9	2C012	CONF							
	2C013	F-BLOCK							
Chip_A	2C014	CONF							
	2C015	F-BLOCK							
Chip_B	2C016	CONF							
	2C017	F-BLOCK							
Chip_C	2C018	CONF							
	2C019	F-BLOCK							
Chip_D	2C01A	CONF							
	2C01B	F-BLOCK							
Chip_E	2C01C	CONF							
	2C01D	F-BLOCK							
Chip_F	2C01E	CONF							
	2C01F	F-BLOCK							
Note: Do not use F-Block 2. *) For E0000, Chip_0 = 32 KB			"	32K: D-9-5-1 16K: E-A-6-2 8K: F-B-7-3 LCIMRO-LCIMRA-RO-RA					